










Issue	Editorial Features					Deadlines		
	See DETAILS for more information on article content					Editorial		Ads
	Routine Columns	Special Feature	Tech Feature	App Feature	Product Guide	Abstract due/First draft due	Editor's final draft due to layout	Ads Close/ Ads Due
December 06/ January 07 Web edition	Altera TI Will Strauss	Annual Buyer's Guide	Non-volatile programmable logic (Flash-based, etc.)	Product round-up	Algorithm development tools	9/04/06 10/02/06	11/06/06	11/27/06 12/04/06
January E-Letter		Mixed signal ICs	DSP & FPGA in wireless applications		Data Acquisition	10/2/06 11/6/06	12/4/06	12/15/06
February/March Web edition	Altera TI	DSP operating systems	 Security solutions: chip- and system-level	 Design tools: EDA, simulation, system-level synthesis	FPGA-based DSP products	11/6/06 12/4/06	1/05/07	02/09/07
February E-Letter		Networking/ Internet	Datacom/telecom		VoIP hardware products	11/6/06 12/4/06	1/05/07	1/09/07
March E-Letter		DSP and FPGA software issue	Debugging tools		Software and development tools	12/4/06 1/05/07	2/5/07	02/19/07
April/May Web edition	Altera TI	Xilinx product roadmap	 IP core interfaces: AMBA, VSIA, etc.	High speed serial in FPGAs: LVDS, SERDES, etc...	Stratix based standard form factor boards	12/4/06 1/05/07	3/5/07	03/30/07 4/07/07
April E-Letter		ESC Silicon Valley review	Medical imaging		ESC announced new products	1/5/07 2/5/07	3/5/07	03/19/07
May E-Letter		DSP and FPGA in military applications	DSP in consumer electronics		DSP resource boards (cPCI, PC/104, VME, PCI)	2/5/07 3/5/07	4/6/07	4/20/07
June/July Web edition	Altera TI Will Strauss	Altera product roadmap	Integrating peripherals with DSP cores	SHARC case studies	 SHARC-based boards	3/5/07 4/6/07	5/4/07	5/15/07 6/01/07
June E-Letter		DSP vs FPGA: vendors speak out	DSPs/FPGAs in industrial automation		FPGAs: • High-density • Mid-density • Low-density	4/6/07 5/4/07	5/4/07	5/18/07
July E-Letter		Unique IP cores, ASICs and SOCs	Algorithms	TI OMAP case studies	IP cores for signal processing	4/6/07 5/4/07	6/01/07	6/15/07
August/September Web edition	Altera TI	Lattice product roadmap	Tradeoffs between hard and soft IP	 Trends in ASICs, ASSPs, and SOCs	Spartan-based standard form factor boards	5/4/07 6/1/07	7/2/07	7/16/07 8/30/07
August E-Letter		Issue dedicated to 3rd party IC companies (Analog Devices, Freescale, TI and others)	Alternative programmable logic devices: FPOA, FPAA, others		Third party development tools	5/4/07 6/1/07	7/2/07	8/30/07
September E-Letter		ESC Boston review	DSP and FPGA in embedded		Video and imaging products			
September PRINT Resource Guide	4th Annual DSP&FPGA.com Product Resource Guide The quest for low power – will portable drive the market? Executive Insight Reserve your Product Entries! DSP & FPGA Resource Boards • Software • DSP & FPGA-based Carrier Boards • FPGAs • DSP Products • EDA tools • Software & cores • Manufacturing • Consulting					6/1/07 7/2/07	8/3/07	8/17/07 9/7/07
October/November Web edition	Altera TI Will Strauss	 IBM's Cell BE processor	Radar and sonar processing	 DSP for military communications	 Rad-hard and space-ready DSP, FPGA and hardware products	7/2/07 8/3/07	9/7/07	10/05/07 09/21/07
October E-Letter		GSPx coverage	DSP in transportation applications		FPGA development Kits	08/03/07 09/07/07	9/7/07	10/5/07
November E-Letter		SDR coverage	DSP in Automotive Applications		Emulators and analyzers	09/07/07 10/05/07	10/5/07	11/2/07
December 07/January 08 Web edition	Altera TI Will Strauss	Annual Buyer's Guide	What's new in audio, imaging, video (JPEG2000, H.264, MPEG4, surround sound, etc.)	 DSP efficiency tools: accelerators, MatLab, specialty cores, etc.	2007's Hottest Editor's Choice Products	11/1/06	11/02/07	11/19/07 12/07/07

Columns: Editor's Foreword, VITA News, VITA Standards Update, VMEbus Technology in Europe, and Military Technology Insider

Departments: Editor's Choice Products, New Products

E-letters: OpenSystems Publishing e-letters share the same theme as each issue's Special Feature and include bonus contributed material



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Sales Office:
30233 Jefferson
St. Clair Shores, MI • 48082
Tel: 586-415-6500 • Fax: 586-415-4882

Editorial Office:
16872 E. Avenue of the Fountains • Suite 203
Fountain Hills, AZ • 85268
Tel: 480-967-5581 • Fax: 480-837-6466

www.vmebus-systems.com
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Issue	Topic	Details
December 06/ January 07 Web edition	Altera TI Will Strauss	These bi-monthly columns from the industry heavyweights are not only insightful, they anchor the market and give readers a view for where technology's headed.
	Annual Buyer's Guide	An on-line virtual catalog resembling paragraph long New Product editorial write-ups. One per vendor, including ICs, cores, boards, development H/W, and S/W tools and applications. Vendors are encouraged to add their products to the DSP-FPGA.com database.
	Non-volatile programmable logic (Flash-based, etc.)	RAM-based FPGAs are great for reconfigurability, but Flash-based ICs offer security from reverse engineering, a known state at power up, and often immunity from soft errors. Editors and contributors will examine the latest devices and applications.
	Product round-up	Contributed articles are sought on the industry's latest products and the unique problems they solve.
	Algorithm development tools	From boards to software to emulators – this Product Guide will showcase tools that aid developers in creating and validating signal processing algorithms.
February/ March Web edition	Altera TI	
	DSP operating systems	The days of writing code that runs "on the metal" are over. Editors will examine operating systems designed to run on DSPs, CPUs (GPPs) and FPGAs running as DSPs, and other specialty devices.
	Security solutions: chip- and system-level	How are devices and code protected from intruders while operating or powered off? Topics may include hardware-level encryption, Trusted Computing, silicon that resists reverse engineering, and other techniques.
	Design tools: EDA, simulation, system-level synthesis	Whether designing an FPGA, SOC, ASSP or ASIC, you can start at VHDL and end up in RTL, or use one of the many higher-level EDA tools to make the job easier. Contributed articles from vendors with Case Study examples are this section's focus.
	FPGA-based DSP products	FPGA-based hardware that's designed for digital signal processing is this Product Guide's focus.
April/May Web edition	Altera TI	
	Xilinx product roadmap	This section of articles will be anchored by the official Xilinx product roadmap (and our editors' "unofficial" predictions). Additionally, Xilinx partners may submit articles on related technical topics such as tools, cores, or products using Xilinx devices.
	IP core interfaces: AMBA, VSIA, etc.	The good news is you don't have to design it yourself if you can find some IP to instantiate in your device. The bad news is: will it work with any other logic in your device? Editor's will examine various IP interface standards, from the popular ARM AMBA to those promoted by the VSI Alliance. Vendors are encouraged to propose articles pertaining to this topic.
	High speed serial in FPGAs: LVDS, SERDES, etc...	From Aurora to PCI Express, next-gen switched fabric interfaces rely on parallel to serial conversion and low-voltage differential signaling. Contributed articles pertaining to LVDS, protocols and serial fabrics are sought.
	Stratix based standard form factor boards	Development and deployable boards for low cost Altera Cyclone devices.
August/ September Web edition	Altera TI	
	Lattice product roadmap	This section of articles will be anchored by the official Lattice product roadmap (and our editors' "unofficial" predictions). Additionally, Lattice partners may submit articles on related technical topics such as tools, cores, or products using Lattice devices.
	Tradeoffs between hard and soft IP	Editors will examine the pros and cons of using soft IP (think Nios or MicroBlaze) and hard IP (think ARM). IP and tools vendors are encouraged to propose technical articles and be available for interview.
	Trends in ASICs, ASSPs, and SOCs	Are these kinds of devices dead? Has programmable logic all but relegated these designs to 1 MU consumer products like cell phones and DVD players? Articles in this section examine the trends.
	Spartan-based standard form factor boards	Development tools and products for low cost Xilinx Spartan FPGAs.
September PRINT Resource Guide	4th Annual DSP&FPGA.com Product Resource Guide The quest for low power – will portable drive the market? Executive Insight Reserve your Product Entries! DSP & FPGA Resource Boards • Software • DSP & FPGA-based Carrier Boards • FPGAs • DSP Products • EDA tools • Software & cores • Manufacturing • Consulting	OSP's editors and contributing columnists weigh in on low power. Contributed articles are sought pertaining to low power products, devices, and software solutions. Other trends: the quest for integration – from system down to chip-level. How does this affect overall power? And: batteries, DC-DC converters, and specialty power-conversion products.
October/ November Web edition	Altera TI Will Strauss	
	IBM's Cell BE processor	By October of 2007 the Cell BE will have been shipping in the Sony PS3 for nearly a year. Has it caught on in the general market? Editors will investigate. Contributors are encouraged to propose technical articles pertaining to Cell-based products, tools and systems.
	Radar and sonar processing	Articles on DSP implementations, algorithms, and radar/sonar systems.
	DSP for military communications	The DoD's digitized battlefield relies on interconnected weapons, humans, and other "assets". And all this relies on Software Defined Radios based upon DSP. Articles will peek at the state of DSP-based mil comms equipment and products.
	Rad-hard and space-ready DSP, FPGA and hardware products	This Product Guide will list the latest chips, boards, and systems that can withstand the rigors of space.
December 07/ January 08 Web edition	Altera TI Will Strauss	
	Annual Buyer's Guide	An on-line virtual catalog resembling paragraph long New Product editorial write-ups. One per vendor, including ICs, cores, boards, development H/W, and S/W tools and applications. Vendors are encouraged to add their products to the DSP-FPGA.com database.
	What's new in audio, imaging, video (JPEG2000, H.264, MPEG4, surround sound, etc.)	Contributed technical articles sought pertaining to DSP used in audio, video and other multimedia applications.
	DSP efficiency tools: accelerators, MatLab, specialty cores, etc.	Editors will examine development tools that make the job of getting algorithms into chips easier, including simplifying the developer's code-writing task.
	2007's Hottest Editor's Choice Products	A listing with commentary of our favorite DSP and FPGA products from the last 12 months – from all of OSP's magazines.